# **Uniden SSE17 LCD initialisation sequence**

v1.2 6 Feb 2021

Phone was manufactured about 2008.

# LCD 18 pin interface : ,,,,,,,,GND, Vcc, Si, Clk, C/D, Reset, CS

GND 0V Vcc +3.3V Si Serial data Clk Serial clock C/D Command/Data

Reset Reset CS Chip select

Display is a graphics module, 132 by 48 pixels

The main alphanumeric font used is 6x16 pixels (including inter-character space pixels). The display is logically divided into two sections, the left 36 columns, and the right 96 columns. In the left section the number of messages stored is depicted by a 7 segment numeric font emulation, 16x32 pixels.

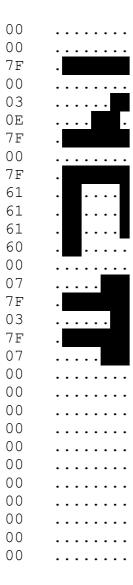
## **LCD** initialisation:

A2	<i>A0</i>	C8 23 81 34 2F A6
A2		LCD Bias Set
ΑO		ADC Select - Normal
С8		Common output mode select - Reverse
23		V5 regulator ratio
81	34	Contrast set
2F		Power control set
A6		Display normal/reverse – normal
D5		EE D2 00 A4 AD 00 AC 40
<i>D5</i> D5		
		EE D2 00 A4 AD 00 AC 40  Clear read-modify-write
D5	00	
D5 EE	00	
D5 EE D2	00	Clear read-modify-write
D5 EE D2 A4	00	Clear read-modify-write  Display all points - OFF

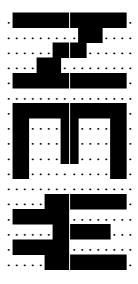
```
B0 10 00 Page Address Set - Cursor(0,0x00)
00 00 00 00 00 00 00 FE 70 CO 00 FE 00 FE 86 86 86 06 00 FE 00
F8 00 FE 00 00 00 00 00 00 00 00 00 00
Draw top half of the letters
00
      . . . . . . . .
00
      . . . . . . . .
0.0
00
00
00
00
      . . . . . . . .
00
FE
70
C0
00
FE
00
FE
86
86
86
06
00
FE
00
F8
00
FE
00
00
00
00
00
00
00
00
00
00
      . . . . . . . .
00
      . . . . . . . .
           Page Address Set - Cursor(1,0x00)
00 00 00 00 00 00 00 00 7F 00 03 0E 7F 00 7F 61 61 61 60 00 07 7F
03 7F 07 00 00 00 00 00 00 00 00 00 00 00
Draw bottom half of the letters
00
      . . . . . . . .
00
00
00
00
      . . . . . . . .
```

00

. . . . . . . .



Together the two rows display " NEW " as depicted here sideways, (there is no gap between the rows on the actual LCD display)



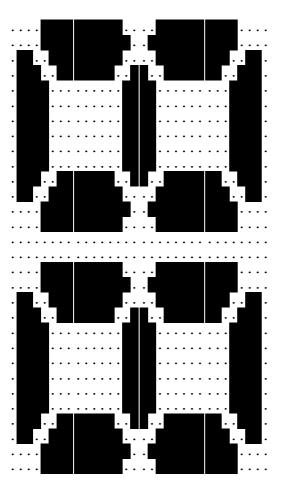
On the left hand part of the display numerals are displayed in a "7 segment" digit format that uses 4 rows of graphics memory for one line of digits.

```
Page Address Set - Cursor(2,0x00)
B2 10 00
00 F0 F0 E6 CE 1E 1E 1E 1E 1E 1E CE E6 F0 F0 00 00 F0 F0 E6 CE 1E 1E
1E 1E 1E 1E CE E6 F0 F0 00 00 00 00 00
B3 10 00
          Page Address Set - Cursor(3,0x00)
00 3F 7F 3F 9F CO CO CO CO CO GO 9F 3F 7F 3F 00 00 3F 7F 3F 9F CO CO
CO CO CO CO 9F 3F 7F 3F 00 00 00 00
         Page Address Set - Cursor(4,0x00)
B4 10 00
00 FC FE FC F9 03 03 03 03 03 F9 FC FE FC 00 00 FC FE FC F9 03 03
03 03 03 03 F9 FC FE FC 00 00 00 00 00
          Page Address Set - Cursor(5,0x00)
B5 10 00
00 OF OF 67 73 78 78 78 78 78 78 78 78 79 0F 0F 0F 0O 0O 0F 0F 67 73 78 78
78 78 78 78 73 67 OF OF OO OO OO OO
```

I haven't shown each byte -> pixel sequence here and the following displays, and have also tended to leave off the leading and trailing spaces. But here the idea is the same as for "NEW" above, but using four rows of graphics to build up the display of two digits.

So the four successive rows form two digits as depicted here sideways (there is no gap between the rows on the actual LCD display)

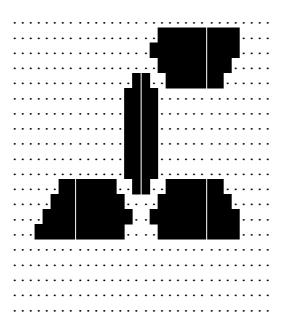




```
D2 00 A4 AD 00 AC 40
D2 00
         Display all points - OFF
Α4
AD 00
         Static indicator - ON, Display state = OFF (no blinking)
AC
         Static indicator - OFF
         Display start line set - 0x00
40
B0 12 04
         Page Address Set - Cursor(0,0x24)
F8 00 FE 00 C0 60 60 60 C0 00 00 00 6 FE 00 00 C0 60 60 60 C0 00 C0 60 60 C0
00 E0 60 C0 60 C0 00 C0 60 60 60 C0 00 00 00 FC 00 00 00 00 00 00 00 00 00 00
B1 12 04
         Page Address Set - Cursor(1,0x24)
03 7F 07 00 3F 66 66 66 37 00 00 00 7F 00 00 3F 60 60 60 39 00 3F 60 60 60 3F
00 7F 00 1F 00 7F 00 3F 66 66 66 37 00 00 00 6F 00 00 00 00 00 00 00 00 00 00
Welcome!
```

```
B2 12 04
        Page Address Set - Cursor(2,0x24)
00 00 00 00 00 00 00 FE 86 86 86 FC 00 00 00 6 FE 00 00 CO 60 60 60 CO 00 CO 60
60 60 C0 00 C0 60 60 60 C0 00 C0 60 60 60 C0 00 00 00 00 00 00 FE 00 F8 00 FE
00 C0 60 60 60 C0 00 00 00 E6 00 00 00 60 FE 60 60 00 00 00 00 00 00 00 00 00 00
B3 12 04
        Page Address Set - Cursor(3,0x24)
00 00 00 00 00 00 00 7F 01 01 01 00 00 00 00 7F 00 00 3F 66 66 66 37 00 3C 66
66 66 7F 00 31 62 66 64 38 00 3F 66 66 66 37 00 00 00 00 00 00 07
                                               7F 03 7F
00 3C 66 66 66 7F 00 00 00 7F 00 00 00 3F 60 60 00 00 00 60 60 00 00 00
" Please Wait... "
(I haven't rendered the byte->pixel image)
        Page Address Set - Cursor(4,0x24)
B4 12 04
Page Address Set - Cursor(5,0x24)
"
AF F0 A2 A0 C8 23 81 34 2F A6 D5 00 EE D2 00 A4 AD 00 AC 40
ΑF
        Display ON
F0
        CD Bias Set
A2
        ADC Select - Normal
AΩ
C8
        Common output mode select - Reverse
23
        V5 regulator ratio
81 34
        Contrast set
2F
        Power control set
        Display normal/reverse - normal
Α6
D5 00
        Clear read-modify-write
EE
D2 00
        Display all points - OFF
A 4
        Static indicator - ON, Display state = OFF (no blinking)
AD 00
AC
        Static indicator - OFF
40
        Display start line set - 0x00
B0 10 00
        Page Address Set - Cursor(0,0x00)
00 00 00 00 00 00 00 00
        Page Address Set - Cursor(1,0x00)
B1 10 00
```

#### " 4 "



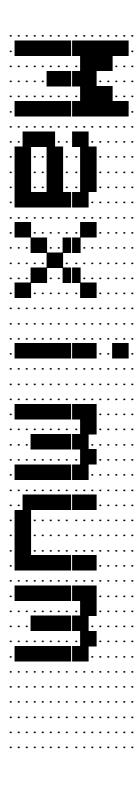
```
D2 00 A4 AD 00 AC 40
D2 00
A 4
       Display all points - OFF
       Static indicator - ON, Display state = OFF (no blinking)
AD 00
       Static indicator - OFF
AC
       Display start line set - 0x00
40
       Page Address Set - Cursor(0,0x24)
00 06 06 FE 06 06 00 CO 60 60 60 CO 00 00 06 FE 00 00 FE 00 80 40 20 00 00
B1 12 04
       Page Address Set - Cursor(1,0x24)
00 00 00 7F 00 00 00 3C 66 66 66 7F 00 00 00 7F 00 00 7F 03 04 18 60 00 00 00
```

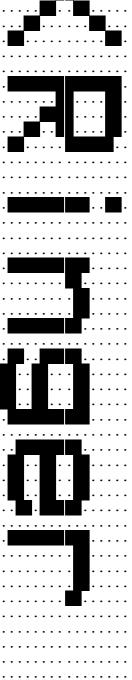
#### "Talk "

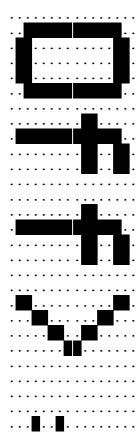
(I haven't rendered the byte->pixel image)

. . . . . .

. . . . . . . . . . . . . . . .







```
AF FO A2 AO C8 23 81 34 2F A6 D5 00 EE D2 00 A4 AD 00 AC 40
AF
          Display ON
F0
          LCD Bias Set
Α2
          ADC Select - Normal
Α0
          Common output mode select - Reverse
С8
23
          V5 regulator ratio
81 34
          Contrast set
2F
          Power control set
          Display normal/reverse - normal
A6
D5 00
EE
          Clear read-modify-write
D2 00
          Display all points - OFF
Α4
          Static indicator - ON, Display state = OFF (no blinking)
AD 00
AC
          Static indicator - OFF
          Display start line set - 0x00
40
B0 10 00
          Page Address Set - Cursor(0,0x00)
00 00 00 00 00 00 00 00
B1 10 00
          Page Address Set - Cursor(1,0x00)
00 00 00 00 00 00 00 00
B2 10 00
          Page Address Set - Cursor(2,0x00)
CO EO FO FO OO OO OO OO
B3 10 00
          Page Address Set - Cursor(3,0x00)
9F 3F 7F 3F 00 00 00 00 00
B4 10 00
          Page Address Set - Cursor(4,0x00)
F9 FC FE FC 00 00 00 00 00
B5 10 00
          Page Address Set - Cursor(5,0x00)
03 07 OF 1F 00 00 00 00 00
" 4 "
(I haven't rendered the byte->pixel image)
AF FO A2 AO C8 23 81 34 2F A6 D5 00 EE D2 00 A4 AD 00 AC 40
AF
          Display ON
F0
          LCD Bias Set
Α2
          ADC Select - Normal
Α0
          Common output mode select - Reverse
C8
23
          V5 regulator ratio
          Contrast set
81 34
          Power control set
2F
          Display normal/reverse - normal
Α6
D5 00
          Clear read-modify-write
EE
D2 00
```

Α4

AC

40

AD 00

Display all points - OFF

Display start line set - 0x00

Static indicator - OFF

Static indicator - ON, Display state = OFF (no blinking)

```
B0 10 00
       Page Address Set - Cursor(0,0x00)
00 00 00 00 00 00 00 00
       Page Address Set - Cursor(1,0x00)
00 00 00 00 00 00 00 00
       Page Address Set - Cursor(2,0x00)
B2 10 00
CO EO FO FO OO OO OO OO
       Page Address Set - Cursor(3,0x00)
9F 3F 7F 3F 00 00 00 00 00
B4 10 00
       Page Address Set - Cursor(4,0x00)
F9 FC FE FC 00 00 00 00 00
B5 10 00
       Page Address Set - Cursor(5,0x00)
03 07 OF 1F 00 00 00 00 00
" 4 "
(I haven't rendered the byte->pixel image)
AF FO A2 AO C8 23 81 34 2F A6 D5 00 EE D2 00 A4 AD 00 AC 40
       Display ON
ΑF
F0
A2
       LCD Bias Set
AΩ
       ADC Select - Normal
       Common output mode select - Reverse
C8
23
       V5 regulator ratio
81 34
       Contrast set
2F
       Power control set
Α6
       Display normal/reverse - normal
D5 00
       Clear read-modify-write
EΕ
D2 00
       Display all points - OFF
Α4
AD 00
       Static indicator - ON, Display state = OFF (no blinking)
       Static indicator - OFF
AC
40
       Display start line set - 0x00
B0 10 00
       Page Address Set - Cursor(0,0x00)
00 00 00 00 00 00 00 00
       Page Address Set - Cursor(1,0x00)
00 00 00 00 00 00 00 00
B2 10 00
       Page Address Set - Cursor(2,0x00)
CO EO FO FO OO OO OO OO
       Page Address Set - Cursor(3,0x00)
B3 10 00
9F 3F 7F 3F 00 00 00 00 00
B4 10 00
       Page Address Set - Cursor(4,0x00)
F9 FC FE FC 00 00 00 00 00
       Page Address Set - Cursor(5,0x00)
```

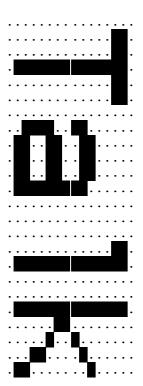
03 07 OF 1F 00 00 00 00 00

#### " 4 "

### (I haven't rendered the byte->pixel image)

```
AF FO A2 AO C8 23 81 34 2F A6 D5 00 EE D2 00 A4 AD 00 AC 40
AF
        Display ON
F0
        LCD Bias Set
Α2
A0
        ADC Select - Normal
        Common output mode select - Reverse
C8
23
        V5 regulator ratio
        Contrast set
81 34
        Power control set
2F
        Display normal/reverse - normal
Α6
D5 00
        Clear read-modify-write
D2 00
        Display all points - OFF
Α4
        Static indicator - ON, Display state = OFF (no blinking)
AD 00
AC
        Static indicator - OFF
40
        Display start line set - 0x00
B0 12 04
        Page Address Set - Cursor(0,0x24)
00 06 06 FE 06 06 00 CO 60 60 60 CO 00 00 06 FE 00 00 FE 00 80 40 20 00 00
B1 12 04
        Page Address Set - Cursor(1,0x24)
00 00 00 7F 00 00 00 3C 66 66 66 7F 00 00 00 7F 00 00 7F 03 04 18 60 00 00 00
```

## "Talk



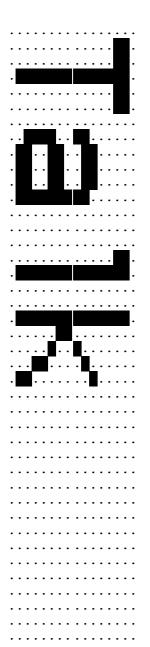
```
Page Address Set - Cursor(2,0x24)
B2 12 04
Page Address Set - Cursor(3,0x24)
B4 12 04
     Page Address Set - Cursor(4,0x24)
Page Address Set - Cursor(5,0x24)
(I haven't rendered the byte->pixel image)
AF FO A2 AO C8 23 81 34 2F A6 D5 00 EE D2 00 A4 AD 00 AC 40
AF
     Display ON
F0
A2
     LCD Bias Set
     ADC Select - Normal
Α0
     Common output mode select - Reverse
C8
23
     V5 regulator ratio
81 34
     Contrast set
2F
     Power control set
A 6
     Display normal/reverse - normal
D5 00
     Clear read-modify-write
ΕE
D2 00
     Display all points - OFF
Α4
AD 00
     Static indicator - ON, Display state = OFF (no blinking)
AC.
     Static indicator - OFF
     Display start line set - 0x00
40
B0 12 04
     Page Address Set - Cursor(0.0x24)
00 06 06 FE 06 06 00 CO 60 60 60 CO 00 00 00 FE 00 00 FE 00 80 40 20 00 00 00
30 00 00 00 FC 06 86 66 FC 00 FC 06 86 66 FC
B1 12 04
     Page Address Set - Cursor(1,0x24)
00 00 00 7F 00 00 00 3C 66 66 66 7F 00 00 00 7F 00 00 7F 03 04 18 60 00 00 00
OC 00 00 00 3F 66 61 60 3F 00 3F 66 61 60 3F
```

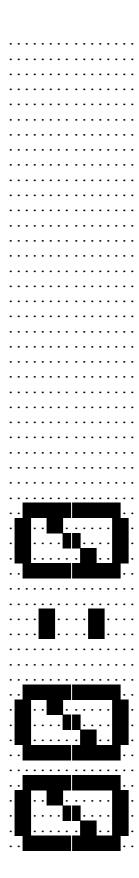
Page Address Set - Cursor(2,0x24)

Page Address Set - Cursor(3,0x24)

B3 12 04

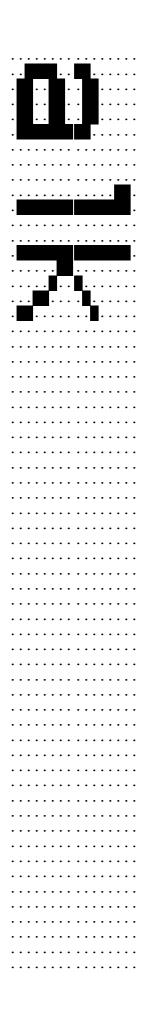
"Talk 0:00"
" "

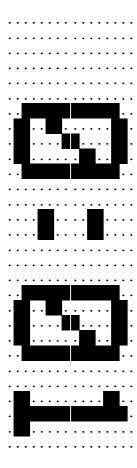




```
AF FO A2 A0 C8 23 81 34 2F A6 D5 00 EE D2 00 A4 AD 00 AC 40
     Display ON
ΑF
F0
     LCD Bias Set
A2
AΩ
     ADC Select - Normal
С8
     Common output mode select - Reverse
23
     V5 regulator ratio
81 34
     Contrast set
2F
     Power control set
     Display normal/reverse - normal
A6
D5 00
EΕ
     Clear read-modify-write
 00
D2
     Display all points - OFF
Α4
     Static indicator - ON, Display state = OFF (no blinking)
AD 00
AC
     Static indicator - OFF
40
     Display start line set - 0x00
B0 12 04
     Page Address Set - Cursor(0,0x24)
00 06 06 FE 06 06 00 CO 60 60 60 CO 00 00 06 FE 00 00 FE 00 80 40 20 00 00
30 00 00 00 FC 06 86 66 FC 00 00 0C FE 00 00
B1 12 04
     Page Address Set - Cursor(1,0x24)
00 00 00 7F 00 00 00 3C 66 66 66 7F 00 00 00 7F 00 00 7F 03 04 18 60 00 00 00
0.0
OC 00 00 00 3F 66 61 60 3F 00 00 60 7F 60 00
B2 12 04
     Page Address Set - Cursor(2,0x24)
B3 12 04
     Page Address Set - Cursor(3,0x24)
B4 12 04
     Page Address Set - Cursor(4,0x24)
0.0
B5 12 04
     Page Address Set - Cursor(5,0x24)
"Talk
     0:01"
**
"
       "
```

. . . . . . . . .





AF